

REMARKS

Claim 27 is pending in the above-identified application. Claims 1-26 were previously cancelled. (Sec, July 25, 2003 Preliminary Amendment).

In the Office Action of February 21, 2008, claim 27 was rejected.

With this Amendment, claim 27 is amended. Accordingly, claim 27 is at issue.

I. 35 U.S.C. § 102 Anticipation Rejection of Claims

Claim 27 was rejected under 35 U.S.C. § 102(b) as being anticipated by *Ogishi Takeshi* (JP 04-241586)(“*Takeshi*”). Applicant respectfully traverses this rejection.

In relevant part, independent claim 27 recites:

“a horizontal scanning circuit element operatively connected to said reset element and said select switch by separate signal lines and effective to generate and deliver said reset pulse and said select pulse.”

This is clearly unlike *Takeshi*, which fails to disclose reset and select pulses connected to a horizontal scanner by separate signal lines. Instead, *Takeshi* discloses a horizontal scanner which generates a reset and select pulse on the same signal line. See, JP 04-241586 Fig. 1.

As the Applicant’s specification discloses, by providing separate signal lines for the reset and signal pulses, the reset pulse can be generated at the midpoint of the scan pulse which eliminates the characteristic deviation in the amplifying MOS transistor. See, U.S. Pat. Pub. No. 2005/0088548, Para. [0042].

Therefore, because *Takeshi* fails to disclose or even fairly suggest all of the features of the claim 27, the rejection cannot stand.

II. Conclusion

In view of the above amendments and remarks, Applicant submits that all claims are clearly allowable over the cited prior art, and respectfully requests early and favorable notification to that effect.

Respectfully submitted,

Dated: May 8, 2008

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